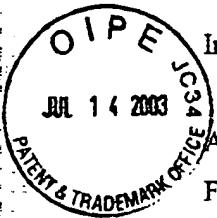


10/6/17874

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:
Janardhanan S. AJIT
Appl. No.: To Be Assigned
Filed: Herewith

For: **I/O Circuit Using Low Voltage
Transistors Which Can Tolerate High
Voltages Even When Power Supplies**

Confirmation No.: To Be Assigned
Art Unit: To Be Assigned
Examiner: To Be Assigned
Atty. Docket: 1875.2120003

**Authorization To Treat A Reply As Incorporating An Extension Of
Time Under 37 C.F.R. § 1.136(a)(3)**

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 19-0036, if such fees are not otherwise provided for in such reply.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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Date: July 14, 2003

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